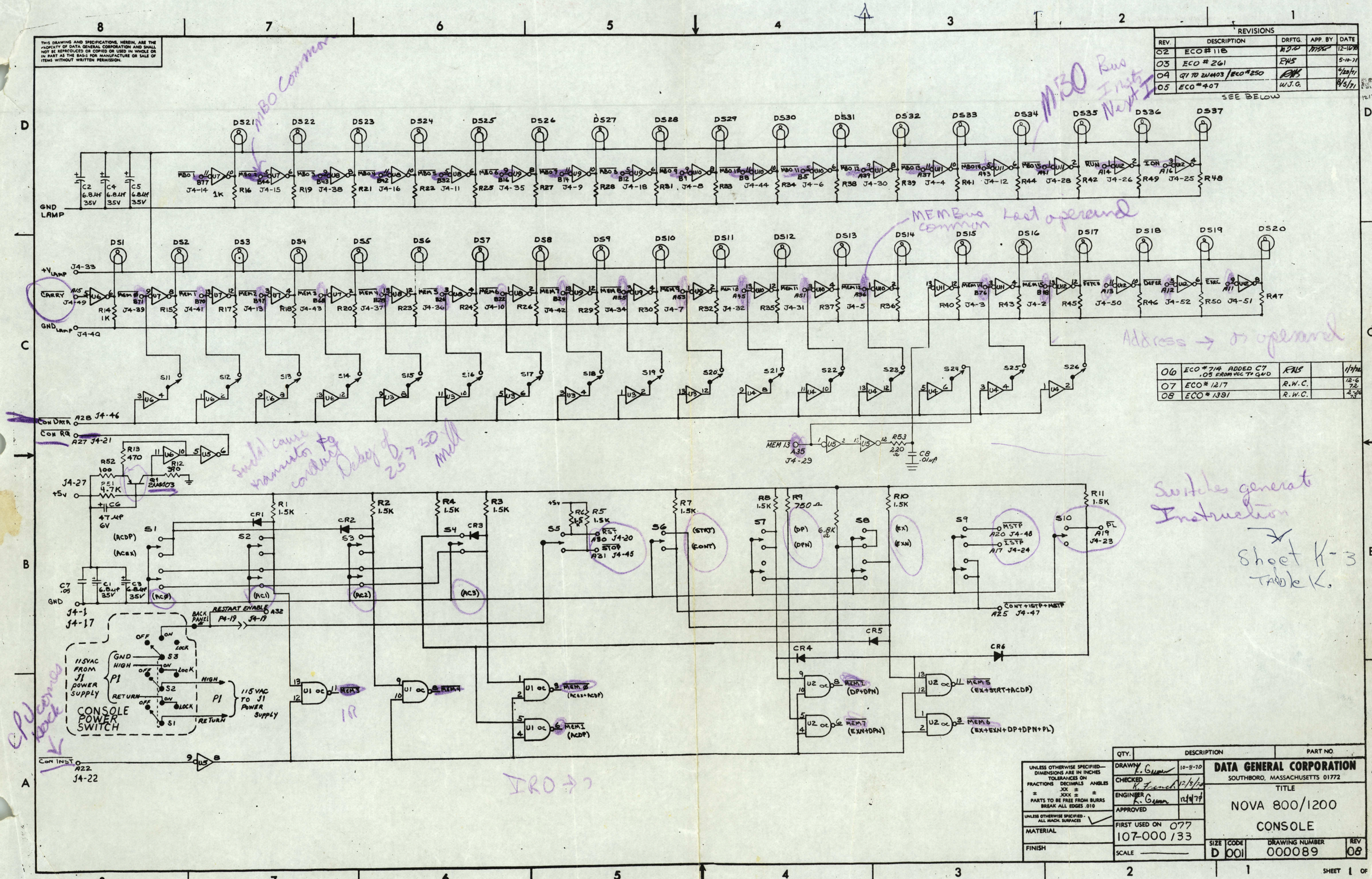

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REVISIONS
REV. | DESCRIPTION | DRFTG. | APP. BY | DATE
02 | ECO # 118 | | | 12-16-70
03 | ECO # 261 | RHS | | 5-10-71
04 | Q1 TO 2N4403 / ECO # 250 | RHS | | 6/28/71
05 | ECO # 407 | W.J.G. | | 8/5/71
SEE BELOW
MBO Common
MBO Bus Insts Next I
MEM Bus common Last operand
Address → or operand
Switches generate Instruction
Sheet K-3 Table K.
Switch cause transistor to conduct Delay of 25 to 30 mill
CPU comes back
IRO → ?
GND LAMP
+V LAMP
CARRY
GND LAMP
CON DATA
CON RQ
+5V
GND
CON INST
115VAC FROM J1 POWER SUPPLY
CONSOLE POWER SWITCH
115VAC TO J1 POWER SUPPLY
RESTART ENABLE
BACK PANEL
MEM 13
MEM 2 (DP+DPN)
MEM 7 (EXN+DPN)
MEM 5 (EX+STRT+ACDP)
MEM 6 (EX+EXN+DP+DPN+PL)
MEM 1 (ACDP)
MEM 0 (ACEX+ACDP)
MEM 3
MEM 4
CONT+ISTP+MSTP
RST
STOP
MSTP
ISTP
PL
RUN
ION
FETCH
DEFER
EXEC
(ACDP)
(ACEX)
(AC0)
(AC1)
(AC2)
(AC3)
(STRT)
(CONT)
(DP)
(DPN)
(EX)
(EXN)
UNLESS OTHERWISE SPECIFIED— DIMENSIONS ARE IN INCHES TOLERANCES ON FRACTIONS DECIMALS ANGLES ± .XX ± .XXX ± ± PARTS TO BE FREE FROM BURRS BREAK ALL EDGES .010
UNLESS OTHERWISE SPECIFIED - ALL MACH. SURFACES
MATERIAL
FINISH
QTY. | DESCRIPTION | PART NO.
DRAWN R. Gunn 10-9-70
CHECKED K. French 12/8/70
ENGINEER R. Gunn 12/8/70
APPROVED
FIRST USED ON 077
107-000/33
SCALE
DATA GENERAL CORPORATION
SOUTHBORO, MASSACHUSETTS 01772
TITLE
NOVA 800/1200
CONSOLE
SIZE D | CODE 001 | DRAWING NUMBER 000089 | REV 08
SHEET 1 OF 1