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REVISIONS				
REV.	DESCRIPTION	DRFTG.	APP. BY	DATE
01	SEE ECO 401			
02	ECO 2628			

1	2	3	4	5	6	7	8	9	10	11	12	13	14
+VINH	RINH	RINH	INH	RINH	INH	RINH	RINH	RINH	INH	RINH	INH	RINH	RINH
	3	4	GND	5	GND	7	6	8	GND	11	GND	10	9
7A10	7A11	7A13	7A14	7A15	7A16	7A17	7A18	7A19	7A20	7A21	7A26	7A23	7A24
RINH	INH	RINH	RINH				IND	IND	RINH	RINH	RINH	IND	RINH
2	GND	1	0				GND	GND	14	12	15	GND	13
7A9	7A8	7A7	7A5				7A30	7A31	7A29	7A28	7A27	7A22	7A25
15	16	17	18	19	20	21	22	23	24	25	26	27	28

Signal Name  
Destination  
Signal Name  
Destination

1	2	3	4	5	6	7	8	9	10
GND	DATOC	DS3	CLR	STRT	DATIC	DATOC	DATOC	DCHA	GND
7A2	7A48	7A46	7A50	7A52	7A54	7A56	7A58	7A60	7A100
GND	DATI	DATIE	INTA	MSKO	DS1	DS2	DS5	DS4	GND
7A1	7A44	7A42	7A40	7A38	7A68	7A66	7A64	7A62	7A99

Signal Name  
Destination  
Signal Name  
Destination

1	2	3	4	5	6	7	8	9	10
MEM	MEM	MBO	MBO	MBO	MBO	MEM	MEM	MEM	MEM
12	13	13	12	15	14	10	11	9	8
7A36	7A35	7A37	7A39	7A41	7A43	7A45	7A51	7A53	7A55
+5	PWR								
(OK)	FAIL								
7A8	7A5								

Signal Name  
Destination  
Signal Name  
Destination

500 4200  
EXPAN Chassis

1	2	3	4	5	6	7	8	9	10
INTP		INTP	DCHP	DCHO	DCHR	DCH1	OVFLO	ROENB	GND
IN		OUT	OUT						
7A96		NOTE	NOTE	7B33	7B35	7B37	7B39	7B41	7B1
DCHP	SELB	SELD	10	DSO	IORST	INTR	DCHM1	DCHM0	GND
OUT			PLS						
7A94	7A82	7A80	7A74	7A72	7B70	7B29	7B21	7B17	7B1

Signal Name  
Destination

QTY	DESCRIPTION	PART NO.
	DRAWN L. KRAY	DATA GENERAL CORPORATION
	CHECKED	
	ENGINEER	
	APPROVED	
	FIRST USED ON	
UNLESS OTHERWISE SPECIFIED— DIMENSIONS ARE IN INCHES TOLERANCES ON FRACTIONS DECIMALS ANGLES XX ± .XX ± .XX PARTS TO BE FREE FROM BURRS BREAK ALL EDGES 010		TITLE Wiring Diagram (8125 & 8225)
UNLESS OTHERWISE SPECIFIED— ALL MACH. SURFACES		SIZE CODE DRAWING NUMBER REV. A 001 000-139 02
MATERIAL	FINISH	SHEET 1 OF 2

Located on Back Panel 107-000-095

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REVISIONS			
REV.	DESCRIPTION	DRFTG.	APP. BY DATE
01	SEC ECO 401		

Located on Back Panel 107-000-095

1	2	3	4	5	6	7	8	9	10
<u>MBO</u> 8	<u>MBO</u> 7	<u>MBO</u> 6	<u>MEM</u> 15	STROB	<u>MEM</u> 6	<u>MEM</u> 7	<u>MEM</u> 5	<u>MEM</u> 4	INHIB
7B12	7B14	7B16	7B18	7B20	7B22	7B24	7B26	7B28	7B30
<u>MBO</u> 9	<u>BMA</u> LOAD	<u>MA</u> LOAD	<u>MBO</u> 10	<u>MBO</u> 11	+VMEN	<u>MBO</u> 3	<u>MBO</u> 2	<u>MBO</u> 4	<u>MBO</u> 5
7B9	7B10	7B7	7B8	7B5	7B46	7B43	7B44	7B42	7B32
11	12	13	14	15	16	17	18	19	20

Signal Name

Destination

1	2	3	4	5	6	7	8	9	10
GND	<u>DATA</u> 5	<u>DATA</u> 12	<u>DATA</u> 0	<u>DATA</u> 4	<u>DATA</u> 13	<u>DATA</u> 9	<u>DATA</u> 2	<u>DATA</u> 6	GND
7B99	7B57	7B59	7B62	7B61	7B64	7B63	7B82	7B95	7B99
GND	<u>DATA</u> 8	<u>DATA</u> 11	<u>DATA</u> 7	<u>DATA</u> 14	<u>DATA</u> 10	<u>DATA</u> 3	<u>DATA</u> 1	<u>DATA</u> 15	GND
7B100	7B60	7B58	7B55	7B56	7B75	7B73	7B65	7B66	7B100
11	12	13	14	15	16	17	18	19	20

Signal Name

Destination

Signal Name

Destination

1	2	3	4	5	6	7	8	9	10
<u>MEM</u> 2	<u>MEM</u> 1	<u>RELD</u> DIS	<u>MEM</u> 0	<u>RELD</u> DIS	MB LD	<u>MEM</u> 14	<u>MBO</u> 1	<u>MBO</u> 0	+VINH
7B47	7B68	7B70	7B71	7B72	7B74	7B76	7B77	7B79	7B84
<u>INHIB</u> TRANS					<u>READ</u> 2	<u>READ</u> 1		<u>INHIB</u> SEL	<u>MB</u> CLEAR
7B45					7B90	7B87		7B85	7B86
11	12	13	14	15	16	17	18	19	20

Signal Name

Destination

Signal Name

NOTE:

1. INTP signal wire wrap as shown on 001-000-138
2. DCHP signal wire wrap as shown on 001-000-138
3. FROM: 20 pin fem connector of assy 005001441.
4. Refer to chas config dwg 01000007.

UNLESS OTHERWISE SPECIFIED— DIMENSIONS ARE IN INCHES TOLERANCES ON FRACTIONS DECIMALS ANGLES ± .XX ± .XXX ± PARTS TO BE FREE FROM BURRS BREAK ALL EDGES 010	QTY	DESCRIPTION	PART NO
	DRAWN L KRAY		
CHECKED			TITLE
ENGINEER			Wiring Diagram
APPROVED			(8125 & 8225)
FIRST USED ON			
MATERIAL			
FINISH			
	SHEET 2 OF 2	SIZE A	CODE 001
		DRAWING NUMBER	000-139
		REV	02