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REVISIONS				
REV.	DESCRIPTION	DRFTG.	APP. BY	DATE
01	ECO # 889	MJG	MJG	4-72

IN-OUT BUS SIGNAL CONNECTIONS

A THRU AF COMPONENT SIDE

(1) THRU (50) WIREWRAP SIDE

Component side	Signal	Wirewrap side
A	GND	(1)
B	+5V	
C	MSKO	A38
D	INTA	A40
E	DATIB	A42
F	DATIA	A44
H	DS3	A46
J	DATOC	A48
K	CLR	A50
L	STRT	A52
M	DATIC	A54
N	DATOB	A56
P	DATOA	A58
R	DCHA	A60
S	DS4	A62
T	DS5	A64
U	DS2	A66
V	DSI	A68
W	IORST	A70
X	DSØ	A72
Y	IOPLS	A74
Z	SELD	A80
a	SELB	A82
b	DCHP OUT	A93
c	INTP OUT	A95
d	DCHMO	B17
e	DCHMI	B21
f	INTR	B29
h	DCHO	B33
j	DCHR	B35
k	DCHI	B37
l	OVFLO	B39
m	RQENB	B41
n	DATA7	B55
p	DATA14	B56
r	DATA5	B57
s	DATA11	B58
t	DATA12	B59
u	DATA8	B60
v	DATA4	B61
w	DATAØ	B62
x	DATA9	B63
y	DATA13	B64
z	DATA1	B65
AA	DATA15	B66
AB	DATA3	B73
AC	DATA10	B75
AD	DATA2	B82
AE	DATA6	B95
AF	GND	(50)

SLOTS 4 THRU 7 ARE TYPICAL TO SLOT 8

Left connector (pins 1–100; odd and even)

Pin	I/O 10	I/O 9	MEMORY OR I/O 8	MEMORY OR I/O 3	MEMORY OR 1220 OPTION 2	CPU 1
1	GND	GND	GND	GND	GND	GND
2	GND	GND	GND	GND	GND	GND
3	+5V	+5V	+5V	+5V	+5V	+5V
4	+5V	+5V	+5V	+5V	+5V	+5V
5			RINHØ	RINHØ	RINHØ	POWER FAIL
6	-5V	-6V	-5V	-5V	-6V	-5V
7			RINH1	RINH1	RINH1	
8			INH GND	INH GND	INH GND	+5 OK
9			RINH2	RINH2	RINH2	MEM OK
10	+V INH	+V INH	+V INH	+V INH	+V INH	+V INH
11			RINH3	RINH3	RINH3	EXEC
12				PTR-B		DEFER
13			RINH4	RINH4	RINH4	FETCH
14			INH GND	INH GND	INH GND	RUN
15			RINH5	RINH5	RINH5	CARRY
16			INH GND	INH GND	INH GND	ION
17			RINH7	RINH7	RINH7	ISTP
18			RINH6	RINH6	RINH6	
19			RINH8	RINH8	RINH8	PL
20			INH GND	INH GND	INH GND	MSTP
21			RINH 11	RINH 11	RINH 11	
22			INH GND	INH GND	INH GND	CON INST
23			RINH 10	RINH 10	RINH 10	
24			RINH9	RINH9	RINH9	
25			RINH 13	RINH 13	RINH 13	CONT+ISTP +MSTP
26			INH GND	INH GND	INH GND	
27			RINH 15	RINH 15	RINH 15	CONRQ
28			RINH 12	RINH 12	RINH 12	CONT DATA
29			RINH 14	RINH 14	RINH 14	
30			INH GND	INH GND	INH GND	RST
31			INH GND	INH GND	INH GND	STOP
32						RESTART ENABLE
33	GND	GND	GND	GND	GND	GND
34	GND	GND	GND	GND	GND	GND
35			MEM13	MEM13	MEM13	MEM13
36			MEM12	MEM12	MEM12	MEM12
37			MBO13	MBO13	MBO13	MBO13
38	MSKO	MSKO	MSKO	MSKO	MSKO	MSKO
39			MBO12	MBO12	MBO12	MBO12
40	INTA	INTA	INTA	INTA	INTA	INTA
41			MBO15	MBO15	MBO15	MBO15
42	DATIB	DATIB	DATIB	DATIB	DATIB	DATIB
43			MBO14	MBO14	MBO14	MBO14
44	DATIA	DATIA	DATIA	DATIA	DATIA	DATIA
45			MEM10	MEM10	MEM10	MEM10
46	DS3	DS3	DS3	DS3	DS3	DS3
47		PIN-17		PTR-C	EXT·LOAD	EXT·LOAD
48	DATOC	DATOC	DATOC	DATOC	DATOC	DATOC
49		PIN-18		PTR-2	FETCH	FETCH
50	CLR	CLR	CLR	CLR	CLR	CLR
51			MEM11	MEM11	MEM11	MEM11
52	STRT	STRT	STRT	STRT	STRT	STRT
53			MEM9	MEM9	MEM9	MEM9
54	DATIC	DATIC	DATIC	DATIC	DATIC	DATIC
55			MEM8	MEM8	MEM8	MEM8
56	DATOB	DATOB	DATOB	DATOB	DATOB	DATOB
57		PIN-16		PTR-4		
58	DATOA	DATOA	DATOA	DATOA	DATOA	DATOA
59		PIN-15		PTR-D		
60	DCHA	DCHA	DCHA	DCHA	DCHA	DCHA
61		PIN-14		PTR-5		
62	DS4	DS4	DS4	DS4	DS4	DS4
63		PIN-13		PTR-6		
64	DS5	DS5	DS5	DS5	DS5	DS5
65		PIN-12		PTR-H		
66	DS2	DS2	DS2	DS2	DS2	DS2
67		PIN-11		PTR-7		
68	DSI	DSI	DSI	DSI	DSI	DSI
69		PIN-10		PTR-J		
70	IORST	IORST	IORST	IORST	IORST	IORST
71		PIN-9		PTR-8		
72	DSO	DSO	DSO	DSO	DSO	DSO
73		PIN-8		PTR-K	LOAD IR	LOAD IR
74	IOPLS	IOPLS	IOPLS	IOPLS	IOPLS	IOPLS
75		PIN-7		PTR-9		
76		PIN-6				
77		PIN-5		PTR-L	LOAD AC	LOAD AC
78		PIN4		EIA-4		
79		PIN-19		EIA-L		
80	SELD	SELD	SELD	SELD	SELD	SELD
81		PIN-20		EIA-5		
82	SELB	SELB	SELB	SELB	SELB	SELB
83		PIN-22				
84		PIN-21				
85		PIN-24		EIA-2	FORCE LOAD IR	FORCE LOAD IR
86		PIN-23				
87		PIN-26		EIA-B	MD SEL1	MD SEL1
88		PIN-25				
89		PIN-27			WAS JSR	WAS JSR
90		PIN-28				
91		PIN-3			DIV	DIV
92		PIN-2			TEST	TEST
93	DCHP OUT	DCHP OUT	DCHP OUT	DCHP OUT		
94	DCHP IN	DCHP IN	DCHP IN	DCHP IN		
95	INTP OUT	INTP OUT	INTP OUT	INTP OUT		
96	INTP IN	INTP IN	INTP IN	INTP IN		
97	+5V	+5V	+5V	+5V	+5V	+5V
98	+5V	+5V	+5V	+5V	+5V	+5V
99	GND	GND	GND	GND	GND	GND
100	GND	GND	GND	GND	GND	GND

Right connector (pins 1–100; odd and even)

Pin	I/O 10	I/O 9	MEMORY OR I/O 8	MEMORY OR I/O 3	MEMORY OR 1220 OPTION 2	CPU 1
1	GND	GND	GND	GND	GND	GND
2	GND	GND	GND	GND	GND	GND
3	+5V	+5V	+5V	+5V	+5V	+5V
4	+5V	+5V	+5V	+5V	+5V	+5V
5			MBO11	MBO11	MBO11	MBO11
6		PIN-29			WHOA	WHOA
7			MA LOAD	MA LOAD	MA LOAD	MA LOAD
8			MBO1Ø	MBO1Ø	MBO1Ø	MBO1Ø
9			MBO9	MBO9	MBO9	MBO9
10						
11		PIN-30		PTP-E		
12			MBO8	MBO8	MBO8	MBO8
13		PIN-31		PTP-4		
14			MBO7	MBO7	MBO7	MBO7
15		PIN-32		PTP-C		
16			MBO6	MBO6	MBO6	MBO6
17	DCHMO	DCHMO	DCHMO	DCHMO	DCHMO	DCHMO
18			MEM15	MEM15	MEM15	MEM15
19		PIN-33				
20			STROBE	STROBE	STROBE	STROBE
21	DCHM1	DCHM1	DCHM1	DCHM1	DCHM1	DCHM1
22			MEM6	MEM6	MEM6	MEM6
23		PIN-34		PTP-3		
24			MEM7	MEM7	MEM7	MEM7
25		PIN-35		PTP-6		
26			MEM5	MEM5	MEM5	MEM5
27		PIN-36		PTP-8		
28			MEM4	MEM4	MEM4	MEM4
29	INTR	INTR	INTR	INTR	INTR	INTR
30			INHIBIT	INHIBIT	INHIBIT	INHIBIT
31		PIN-37				
32			MBO5	MBO5	MBO5	MBO5
33	DCHO	DCHO	DCHO	DCHO	DCHO	DCHO
34		PIN-38		PTP-F		
35	DCHR	DCHR	DCHR	DCHR	DCHR	DCHR
36		PIN-39		PTP-7		
37	DCHI	DCHI	DCHI	DCHI	DCHI	DCHI
38		PIN-40		PTP-H		
39	OVFLO	OVFLO	OVFLO	OVFLO	OVFLO	OVFLO
40		PIN-41		PTP-8		
41	RQENB	RQENB	RQENB	RQENB	RQENB	RQENB
42			MBO4	MBO4	MBO4	MBO4
43			MBO3	MBO3	MBO3	MBO3
44			MBO2	MBO2	MBO2	MBO2
45			INHIBIT TRANSMISSION	INHIBIT TRANSMISSION	INHIBIT TRANSMISSION	INHIBIT TRANSMISSION
46	+V INH	+V INH	+V INH	+V INH	+V INH	
47			MEM2	MEM2	MEM2	MEM2
48		PIN 42		PTP-J	CLK FLOP	CLK FLOP
49		PIN-43		PTP-9	EXT LOAD	EXT LOAD
50	GND	GND	GND	GND	GND	GND
51		PIN-44		PTP-K		
52		PIN 45		EIA-8		
53		PIN-46		EIA-J		
54		PIN 47		EIA-6		
55	DATA7	DATA7	DATA7	DATA7	DATA7	DATA7
56	DATA14	DATA14	DATA14	DATA14	DATA14	DATA14
57	DATA5	DATA5	DATA5	DATA5	DATA5	DATA5
58	DATA11	DATA11	DATA11	DATA11	DATA11	DATA11
59	DATA12	DATA12	DATA12	DATA12	DATA12	DATA12
60	DATA8	DATA8	DATA8	DATA8	DATA8	DATA8
61	DATA4	DATA4	DATA4	DATA4	DATA4	DATA4
62	DATAØ	DATAØ	DATAØ	DATAØ	DATAØ	DATAØ
63	DATA9	DATA9	DATA9	DATA9	DATA9	DATA9
64	DATA13	DATA13	DATA13	DATA13	DATA13	DATA13
65	DATA1	DATA1	DATA1	DATA1	DATA1	DATA1
66	DATA15	DATA15	DATA15	DATA15	DATA15	DATA15
67		PIN-48		PTP-2		
68			MEM3	MEM3	MEM3	MEM3
69		PIN-49		EIA-3		SKIP
70			MEM1	MEM1	MEM1	MEM1
71			MEMØ	MEMØ	MEMØ	MEMØ
72			RELOAD DISABLE	RELOAD DISABLE	RELOAD DISABLE	RELOAD DISABLE
73	DATA3	DATA3	DATA3	DATA3	DATA3	DATA3
74			MB LOAD	MB LOAD	MB LOAD	MB LOAD
75	DATA1Ø	DATA1Ø	DATA1Ø	DATA1Ø	DATA1Ø	DATA1Ø
76			MEM14	MEM14	MEM14	MEM14
77			MBO1	MBO1	MBO1	MBO1
78			ROS	ROS	ROS	
79			MBOØ	MBOØ	MBOØ	MBOØ
80			EXTERNAL SELECT	EXTERNAL SELECT	EXTERNAL SELECT	
81	-5V	-5V	-5V	-5V	-5V	
82	DATA2	DATA2	DATA2	DATA2	DATA2	DATA2
83			READ IO	READ IO	READ IO	READ IO
84	+V INH	+V INH	+V INH	+V INH	+V INH	
85			INHIBIT SELECT	INHIBIT SELECT	INHIBIT SELECT	
86			MB CLEAR	MB CLEAR	MB CLEAR	MB CLEAR
87			READ 1	READ 1	READ 1	READ 1
88			DRIVE IO	DRIVE IO	DRIVE IO	DRIVE IO
89	GND	GND	GND	GND	GND	GND
90			READ 2	READ 2	READ 2	READ 2
91					SHIFT3	SHIFT3
92	GND	GND	GND	GND	GND	GND
93					SHIFT2	SHIFT2
94					SHIFTØ	SHIFTØ
95	DATA6	DATA6	DATA6	DATA6	DATA6	DATA6
96					SHIFT1	SHIFT1
97	+5V	+5V	+5V	+5V	+5V	+5V
98	+5V	+5V	+5V	+5V	+5V	+5V
99	GND	GND	GND	GND	GND	GND
100	GND	GND	GND	GND	GND	GND

4010 TELETYPE CONNECTION SLOT 3

	84	86	88	90	92	94	96	98	100
Odd	+V	TT OUT	DATA IN STOP	RD RUN				+5V	GND
Even								+5V	GND

3A6 O -5V
3B69 O TT IN
WIRE WRAP PINS

QTY.	DESCRIPTION	PART NO.

UNLESS OTHERWISE SPECIFIED— DIMENSIONS ARE IN INCHES TOLERANCES ON FRACTIONS ± DECIMALS .XX ± .XXX ± ANGLES ± PARTS TO BE FREE FROM BURRS BREAK ALL EDGES .010

UNLESS OTHERWISE SPECIFIED - ALL MACH. SURFACES

MATERIAL

FINISH

DRAWN	PERRYMAN	1-5-72
CHECKED		
ENGINEER		
APPROVED		
FIRST USED ON	107-000128	
SCALE		

DATA GENERAL CORPORATION
SOUTHBORO, MASSACHUSETTS 01772

TITLE
BACK PANEL
NOVA 1220

SIZE	CODE	DRAWING NUMBER	REV.
D	001	000208	01

SHEET 1 OF 1