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| REVISIONS | | | | |
|-----------|-------------------|-------|---------|------|
| REV. | DESCRIPTION | DRFTG | APP. BY | DATE |
| 01 | SEC E CO 401/1/79 | | | |

1) Expansion chassis shall be secured below main CPU chassis using the following hardware kit:

| | |
|------------------------------|--------|
| 6-32 Pan Hd. Screw 5/16 long | Qty. 5 |
| 6-32 Keps Nut | Qty. 3 |
| #6 Plain Washer | Qty. 3 |

2) I/O cables from expansion chassis shall be plugged into P4, P6 and P8 on main CPU chassis. If expansion chassis is being connected to another expansion chassis, the connectors are plugged into P3, P5, and P7 of expansion back panel immediately above.

NOTE: If configuration requires external I/O (Option 8122, 8222), it shall be connected to lowest order expansion chassis I/O connectors.

3) Data channel priority, interrupt priority etch drill outs and power fail wiring shall be connected per schematic DGC No. 001-000-138.

4) The above configuration shall be used for expanding I/O only.

| QTY | DESCRIPTION | PART NO |
|--|-------------|--|
| <small>UNLESS OTHERWISE SPECIFIED— DIMENSIONS ARE IN INCHES TOLERANCES ON FRACTIONS DECIMALS ANGLES XX ± XXX ± ± PARTS TO BE FREE FROM BURRS BREAK ALL EDGES 010</small> | | |
| DRAWN L. Kray | | DATA GENERAL CORPORATION |
| CHECKED | | |
| ENGINEER | | TITLE |
| APPROVED | | 8124 & 8224 |
| FIRST USED ON | | EXPANSION CHASSIS CONFIGURATION RULES |
| MATERIAL | | SIZE CODE |
| FINISH | | DRAWING NUMBER |
| SHEET 1 OF 1 | | A 010 |
| | | 000-008 |
| | | REV 01 |