

**DIB** #25

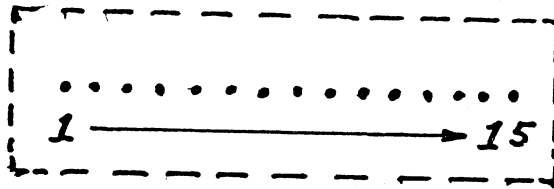
DEPOT INFORMATION BULLETIN

Field Service

Subject:

1210/1220 POWER FAIL MODULE

Submitted by: DAVID GEENE	Date: 2/4/75
Reviewed by: <i>David Geene</i>	Date: 2/4/75


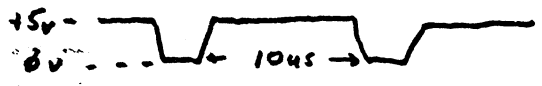
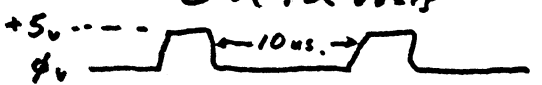
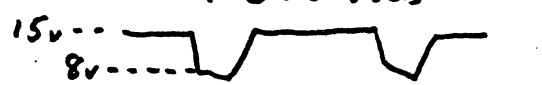


PIN	LOCATION	NAME	COMMENTS
1	A-1	GND	GROUND
2	A-3	+5 VOLTS	} FROM POWER SUPPLY
3	A-6	-5 VOLTS	
4	A-5	PWR FAIL	
5	A-8	+5 OK	} FROM PWR. FAIL MODULE
6	A-9	MEM. OK	
Δ 7	—	SIGNAL FROM BRIDGE RECTIFIER	
8	A-10	+15 VOLTS	} FROM POWER SUPPLY
9	A-13	FETCH	
10	A-35	MEM 13	} FROM 1210/1210 MEMORY
11	—	30 VNR	} FROM POWER SUPPLY
12	B-30	INHIBIT	} FROM CPU
13	A-36	MEM 12	} FROM MEMORY
14	A-28	RINH 12	} FROM MEMORY
* 15	A-47	EXT. LOAD	} FROM POWER FAIL MODULE

\* The pwr.fail module looks for bit 13 coming back from the "bit 13" diode on the memory. If it does not see it; it pulses "ext.load" low and wipes out the accumulators.

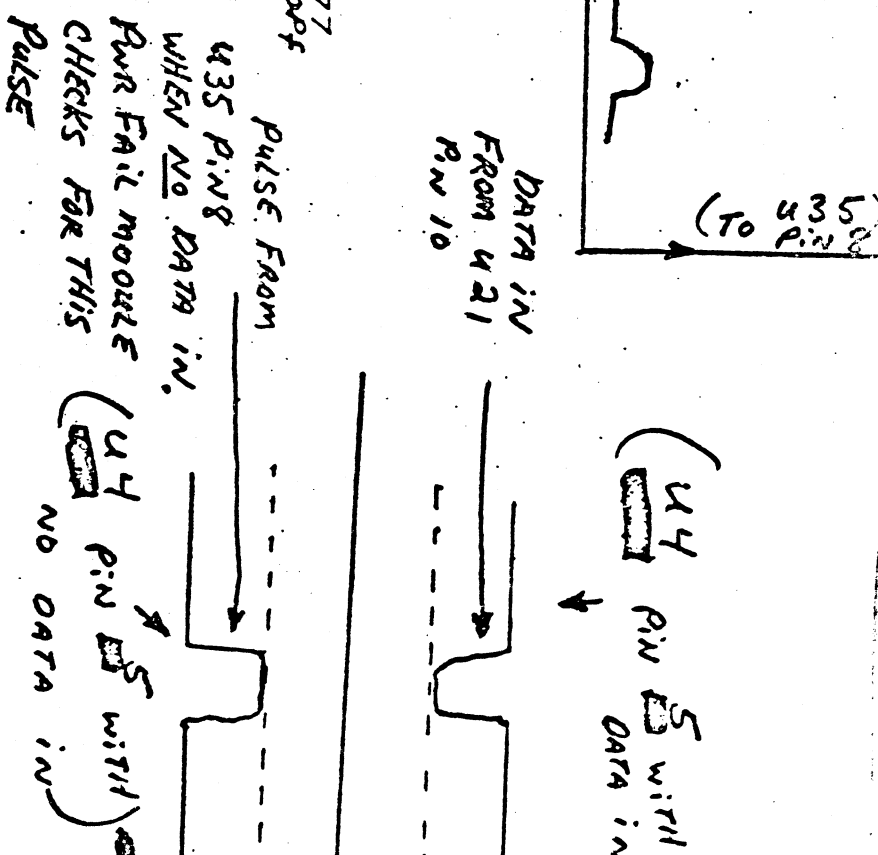
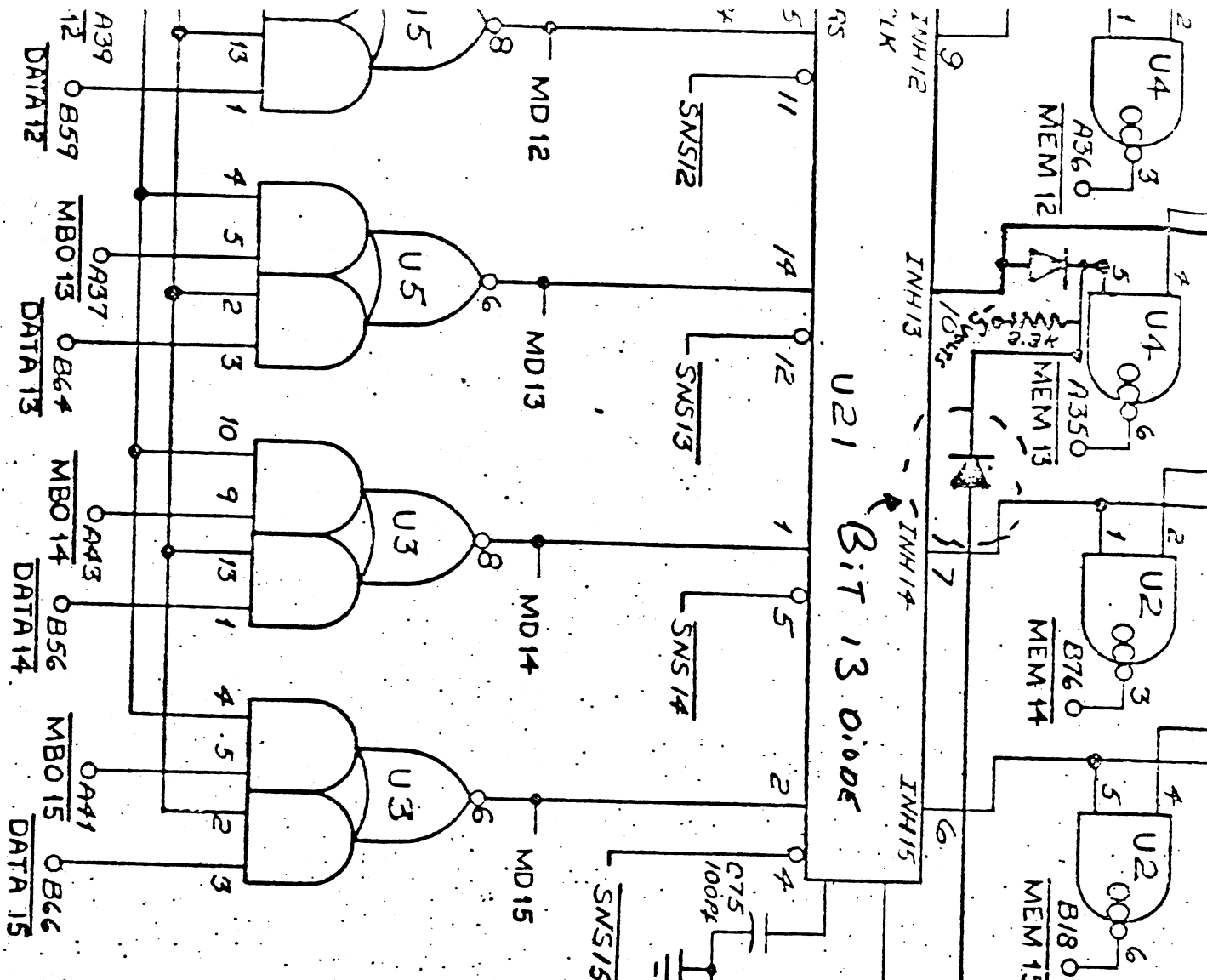
Δ Pin 7 for a 1220 is a 0volt d-c ,60hz signal; for a 1210 it is a 18volt p/p half wave d-c signal.

# 1210/1220 PWR FAIL MODULE

PIN	LOCATION	STATIC D-C. VOLTAGES	VOLTAGES WITH 1210 MEM. ON JMP
1	A-1	GND	GROUND
2	A-3	5.2	5.0 volts
3	A-6	-6.28	-6.01 volts
4	A-5	5.2	5.0 volts
5	A-8	4.4	4.1 volts
6	A-9	5.2	5.0 volts
Δ 7	—	5.58	
8	A-10	15.10	15.0 volts
9	A-13	1.68	0.5 volts
* 10	A-35	1.73	
11	—	32.53	32.2 volts
12	B-30	1.68	
13	A-36	0.32	+ 5.0 volts
14	A-28	15.1	
15	A-47	1.67	5.0 volts

Δ pin 7 static d-c voltage is shown above for a 1210 memory. for a 1220 memory there would be zero volts d-c because it is taken off a different pin on the bridge rectifier.

\* if a 1200 memory was mistakenly placed in a 1210 or 1220 pin 10 would just be a straight +5volt signal with no pulse. Because there is no diode for bit 13 on a 1200 memory.



NOTE:

ALL CAPACITORS, MICROFARADS, AND RESISTORS ARE IN 1/4 WATT, WITH 5% TOLERANCE UNLESS OTHERWISE SPECIFIED.