



5. Wire Bottom H4-5 to top H4-2.

9. Wire C7-2 to L504 E6-1.

10. Wire L504 E6-2 to Top H4-3.

11. Wire C6-1 to Top H4-4.

12. Wire Top H4-1 to Top H4-14.



PRINTED: 11/20/89

SHEET 1 OF 1



REWORK  
ZETACO

FOR: BMX-2/SI

ASSEMBLY #: R500-387-04

REV LEVEL : C

ECO REWORK 0546,0602,0634,0688,0776,0995,1170,1372

## BMX-2/SI REWORK FOR BOARD BLANK NUMBER 041-065-00

1. Lift pin at S2-6.
2. Add wire from S2-8 to S2-6 pad (not lifted pin).
3. Lift J11-6.
4. Add wire J11-6 to G9-3.
5. Cut and lift pin at IC A3-3.
6. Add wire from IC A4-2 to A4-3.
7. Add wire from IC A4-13 to IC A4-2.
8. Add wire form IC A4-1 to lifted pin of A3-3.
9. Lift R8-4.
10. Add wire form L2-3 to R8-4.
11. Add wire from K1-12 to L2-2.
12. Add wire form J1-4 to L2-1.
13. Lift pin F1-6.
14. Wire adds.
  - A) Z3-9 to F1-6. (Lifted pin)
  - B) Z3-10 to L4-3.
  - C) Z3-8 to F2-5.
15. Lift pin 2 of Z3.
16. Wire X4-3 to lifted pin Z3-2.
17. Wire Z4-8 to X4-2.
18. Wire Z4-12 to X4-1.

DEC 04 1989

NOTE: Remember to replace item 103 200-056-00 eeprom set with 200-171-00 same position.

19. Lift Z3-13.
20. Wire Y4-1 to pad Z3-13.
21. Wire Y4-2 to Y4-3.
22. Wire Y4-4 to Z3-13 (lifted pin).
23. Replace L5 (74LS04) with a 74F04.
24. Lift N10-6.
25. Wire adds:
  - a) M8-~~11~~ to P7-12.
  - b) M8-12 to lifted pin N10-6.
  - c) M8-~~13~~ to N9-4.

# ZETA-O DEVIATION AUTHORIZATION

ADVANCE DEVIATION AUTHORIZATION

DEVIATION AUTHORIZATION

DEVIATION NUMBER 136

MODEL NUMBER BMX-2

DEVIATION EFFECTIVITY DATE 11/1/89

DEVIATION TERMINATES ON:

- Date \_\_\_\_\_
- ECO Number \_\_\_\_\_
- Other IMPLEMENT Q.S. DETERMINED NECESSARY BY CUSTOMER SUPPORT AT ENGINEERING.

DEVIATION AFFECTS:

- Hardware  Drawings
- Firmware  Parts List
- Manual(s)  Other \_\_\_\_\_

(SEE Q.A. FOR APPLICABLE SERIAL NUMBERS)

REASON: To minimize a reflection problem on the BMC / DATA signal.


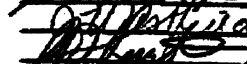




PART / DOCUMENT NUMBER	REVISION	DESCRIPTION
<u>500-457-00</u>	<u>N/A</u>	<u>BMX-2 ASSY (041-116-00)</u>
		<u>THIS APPROX ONLY.</u>

DESCRIPTION (Include Red-Line Mark-ups):

1. Lift pins: Connector J1-33, L11-2, K11-11, L10-1.
2. Wire together the pins listed in step #1. Use the shortest possible route.
3. Install a piece of MYLAR INSULATION BETWEEN THE PINS & PAD @ L11-2

REQUESTOR 

DATE 11/1/89

DEPARTMENT	SIGN	DATE
DESIGN ENGINEERING MGR.		<u>11/1/89</u>
MANUFACTURING		<u>11/1/89</u>
QUALITY ASSURANCE		<u>11/1/89</u>
MARKETING		<u>11/1/89</u>
PRODUCTION CONTROL		<u>11/1/89</u>
CUSTOMER SUPPORT		<u>11/1/89</u>

ZETACO, INC.

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DEVIATION AUTHORIZATION

MODEL NUMBER Bmx-2	EFFECTIVITY DATE 10/19/87	PROJECTED CLEARING DATE 13 boards from WORLD
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DEVIATION AFFECTS:			ECO REQUIRED
<input checked="" type="checkbox"/> HARDWARE	<input type="checkbox"/> DRAWING	<input type="checkbox"/> MANUALS	YES <input type="checkbox"/> NO <input checked="" type="checkbox"/>
<input type="checkbox"/> FIRMWARE	<input type="checkbox"/> PARTS LIST	<input type="checkbox"/>	

REASON FOR DEVIATION:  
 Allow 13 old artwork boards from WORLD to be modified, thus fixing a power-up selftest problem seen with MV7800 CPUs. The rework for these boards requires using a different gate than specified by the ECO generated to solve this problem.

PART/DOCUMENT NUMBER	REVISION	DESCRIPTION
500 387-000		Assembly with board # 100728-000

DESCRIPTION OF DEVIATION

BOARD #s 23365, 66, 67, 68  
 SN 23423, 24, 25, 26, 27, 31, 32, 33, 34  
 23606, 23607, 23608

The following rework is to be performed:

1. Lift pin F1-6
2. Add wire from K6-4 to lifted pin F1-6
3. Add wire from K6-5 to L4-3
4. Add wire from K6-6 to F2-5

APPROVAL		
ORIGINATOR	DEPARTMENT	DATE
Janet B-W	PRODUCT SUPPORT	10/19/87
ENGINEERING MANAGER: Tom Loeffler		10/19/87
MANUFACTURING: Edward		10/20/87
QUALITY ASSURANCE: Ned Dingley		10/21/87
MARKETING: Klance		10/21/87
PRODUCTION CONTROL: Judy Westfield		10/20/87